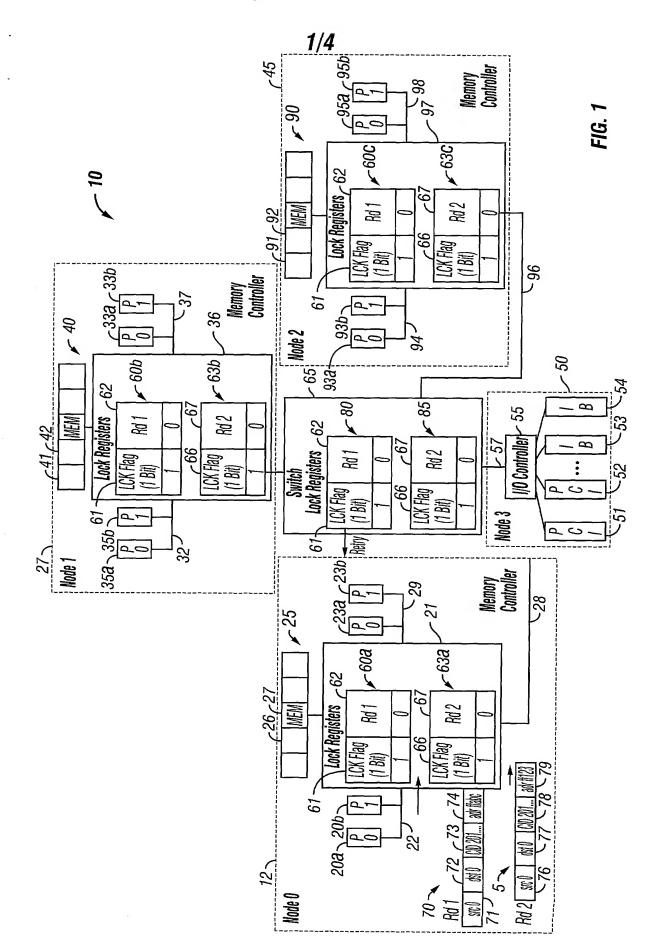
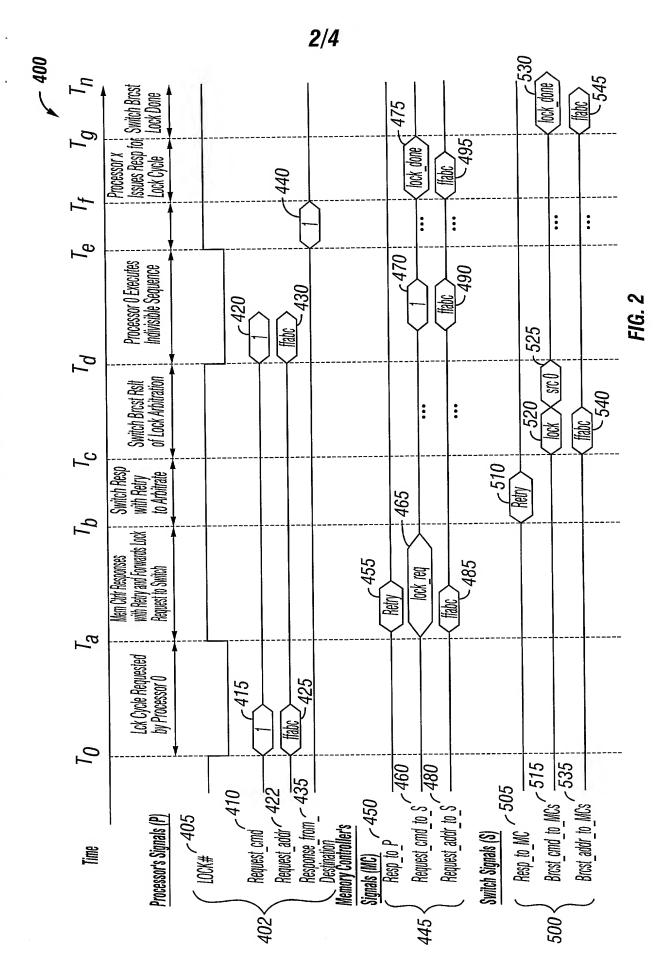
TITLE: LOW LATENCY LOCK FOR MULTIPROCESSOR COMPUTER SYSTEM
IN TOR: Hahn Vo
SERIAL NO.: 10/039,045



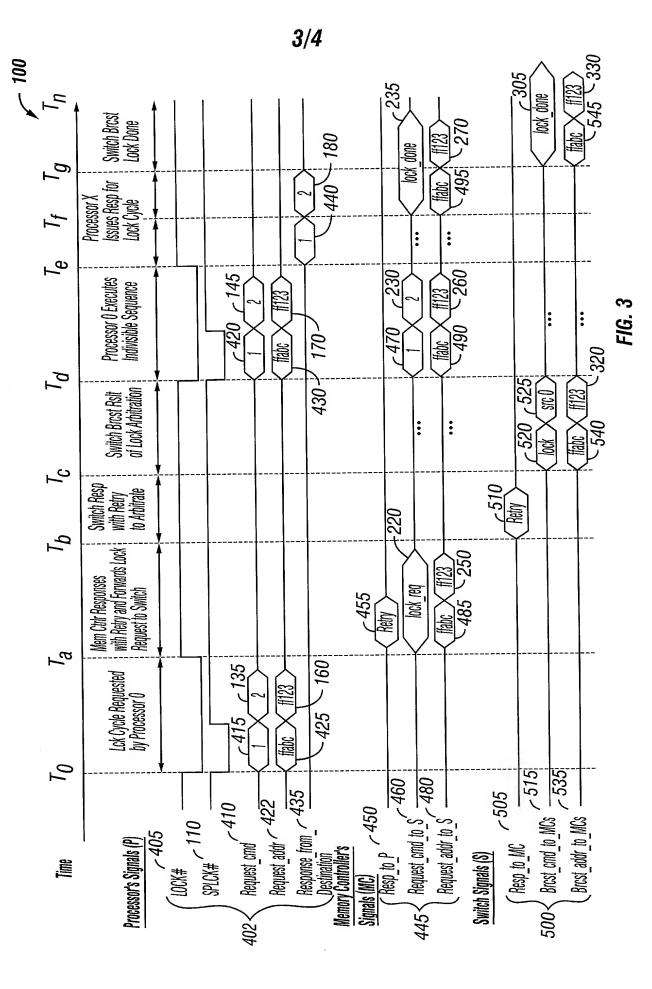
TITLE: LOW LATENCY LOCK FOR MULTIPROCESSOR COMPUTER SYSTEM
Hahn Vo
SERIAL NO.: 10/039,045



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LOW LATENCY LOCK FOR MULTIPROCESSOR COMPUTER SYSTEM Hahn Vo

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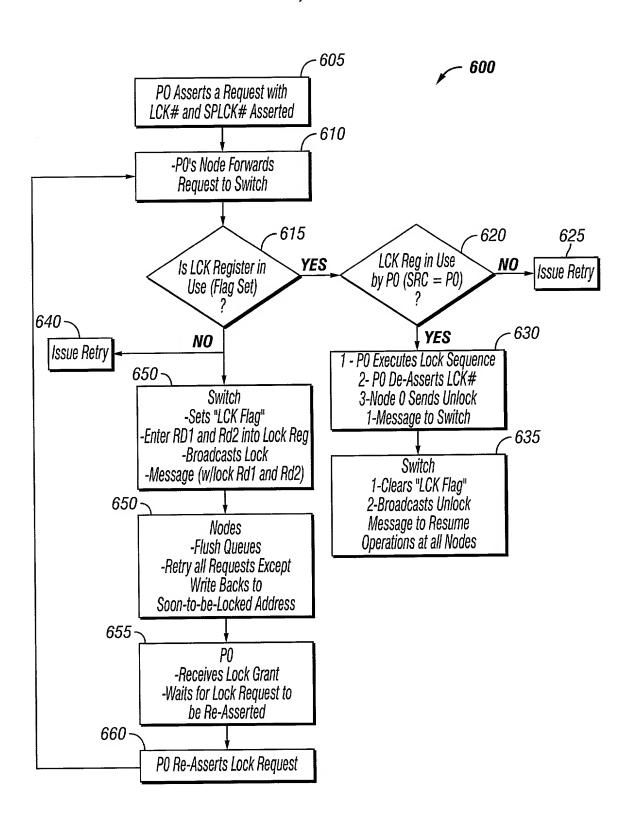


FIG. 4